

SHARED HIGH VOLTAGE POWER SUPPLY FOR IMAGE TRANSFER IN AN IMAGE FORMING DEVICE

BACKGROUND

[001] The invention relates generally to an image forming device, and more particularly, to an image forming device having a shared high voltage power supply.

[002] An image forming device, such as a color printer, typically includes four units associated with four colors, black, magenta, cyan and yellow. Each unit includes a laser printhead that is scanned to provide a latent image on the charged surface of a photoconductive unit. The latent image on each unit is developed with the appropriate color toner and is then transferred to either an intermediate transfer medium or directly to a substrate (such as paper) that travels past the photosensitive units. The resulting full-color image is dependent on the combination of each color toner transferred to the substrate one line at a time. The toner on the substrate is then fused to the substrate in a fuser assembly, and the substrate is transported out of the printer. Thus, in a typical multi-color laser printer, the substrate receives color images generated at each of the four image units.

[003] The image forming device, like all consumer products, should be constructed in an economical manner. Price is one of the leading factors when a user makes a purchasing decision. Further, quality of the resulting product is another factor for users. Cost and quality are thus guiding factors in the design and manufacture of image forming devices.

SUMMARY

[004] The present invention relates to an image transfer assembly for use with an image forming device. The image transfer assembly includes a plurality of image

forming units transferring print material to a media substrate. Each of the image forming units includes a photoconductive unit and a transfer device positioned to receive the media substrate therebetween. The image transfer assembly further includes a first power supply coupled to at least two of the transfer devices supplying a voltage thereto.

[005] According to another aspect of the present invention, a method of printing includes moving a media substrate to a first image forming unit. A first voltage from a first power supply is applied to the first image forming unit facilitating the transfer of a first print material from the first image forming unit to the media substrate. The media substrate is moved to a second image forming unit. The first voltage from said first power supply is applied to the second image forming unit facilitating the transfer of a second print material from the second image forming unit to the media substrate.

BRIEF DESCRIPTION OF DRAWINGS

[006] Figure 1 is a schematic view of one embodiment of an image forming device constructed according to the present invention;

[007] Figure 2 is a schematic view illustrating an image forming unit of the image forming device of Figure 1.

[008] Figure 3 is a block diagram of an image transfer assembly of the image forming device of Figure 1.

[009] Figure 4A is a graph of voltage and transfer current at an image forming unit.

[0010] Figure 4B is a graph of a transfer current threshold indicating signal.

[0011] Figure 5 is a schematic diagram of one embodiment of a transfer current threshold indicating signal generating circuit.

[0012] Figure 6 is a schematic diagram of one embodiment of a power supply used in the image transfer assembly of Figure 3.

DETAILED DESCRIPTION

[0013] Figure 1 depicts a representative image forming device 10. According to one embodiment of the present invention, the image forming device 10 is a color laser printer. Other examples of an image forming device include but are not limited to an ink-jet printer, fax machine, copier or any combination thereof. However, it should be apparent to those skilled in the art that the image forming device 10 may be any device in which an image is formed on a media substrate. The image forming device 10 comprises a main body 12 and a subunit 13. A media tray 14 with a pick mechanism 16, or a manual input 32, are conduits for introducing media substrates in the device 10. The media tray 14 is preferably removable for refilling, and located on a lower section of the device 10.

[0014] Media substrates may comprise paper of any type, transparencies, labels, envelopes and the like. The media substrates are moved from the input and fed into a primary media path. One or more registration rollers 18 disposed along the media path aligns the media substrate and precisely controls its further movement along the media path. A media transport belt 20 forms a section of the media path for moving the media substrates past an image transfer assembly 50. The image transfer assembly 50 includes a plurality of image forming units 100.

[0015] As illustrated in Figure 1, the image forming device 10 includes four image forming units 100 for transferring print material on the media substrate to produce a full-color image. The image forming units 100 are disposed along a vertical plane. However, it will be appreciated by those skilled in the art that the image forming units may be disposed along a horizontal plane or any other orientation. The print material typically comprises toner of varying colors. For illustrative purposes, the image forming units 100 include cyan, magenta, yellow, and black toner to produce a full-color image

on the media substrate.

[0016] An imaging device 22 forms an electrical charge on a photoconductive unit 102 (see Figure 2) within the image forming units 100 as part of the image formation process. The term “imaging device” refers to a device that arranges an electrical charge on the photoconductive unit 102. Various imaging devices may be used such as a laser printhead or a LED printhead. The media substrate with loose toner from one or more of the image forming units 100 is then moved through a fuser 24 that adheres the toner to the media substrate. Exit rollers 26 rotate in a forward or a reverse direction to move the media sheet to an output tray 28 or a duplex path 30. The duplex path 30 directs the inverted media substrate back through the image formation process for forming an image on a second side of the media substrate.

[0017] The image forming units 100 each include an exterior housing 40 that forms a reservoir for holding a supply of toner of each appropriate color. One or more agitating members (not shown) are positioned within the reservoir for agitating and moving the toner towards the media substrate.

[0018] Figure 2 is a schematic diagram illustrating an exemplary image forming unit 100. Each image forming unit 100 includes a photoconductive (PC) unit 102, a charging unit 104, a developer roll 106, a transfer device 108, and a cleaning blade 110. The PC unit 102 is cylindrically shaped and illustrated as a drum. However, it will be apparent to those skilled in the art that the PC unit 102 may comprise any appropriate structure. The charging unit 104 charges the surface of the PC unit 102 to a negative potential, approximately -1000 volts in the present embodiment. A laser beam 112 from the imaging device 22 (see Figure 1) discharges areas on the PC unit 102 to form a latent image on the surface of the PC unit 102. The areas of the PC unit 102 illuminated by the laser beam 112 are discharged resulting in a potential of approximately -300 volts in the present embodiment. The PC unit core is held at approximately -200 volts while the

transfer device 108 is charged at a predetermined positive potential.

[0019] In the present invention, the potential of the transfer device 108 may vary depending on the type of media substrate and the color of the toner being applied to the media substrate as discussed further herein. The developer roll 106 transfers negatively-charged toner having a core voltage of approximately -600 volts to the surface of the PC unit 102 to develop the latent image on the PC unit 102. The toner is attracted to the most positive surface, i.e., the area discharged by the laser beam 112. As the PC unit 102 rotates, a positive voltage field produced by the transfer device 108 attracts and transfers the toner on the PC unit 102 to the media substrate. Alternatively, the toner images could be transferred to an intermediate transfer member (ITM) and subsequently from the ITM to the media substrate. Any remaining toner on the PC unit 102 is then removed by the cleaning blade 110. The transfer device 108 may include a roll, a transfer corona, transfer belt, or multiple transfer devices, such as multiple transfer rolls. The area between the PC unit 102 and the transfer device 108 is known as a transfer nip.

[0020] Referring now to Figure 3, the image transfer assembly 50 comprises four image forming units 100A-100D. In one embodiment of the present invention, the first image forming unit 100A contains black toner, the second image forming unit 100B contains yellow toner, the third image forming unit 100C contains magenta toner and the fourth image forming unit 100D contains cyan toner. However, it will be apparent to those skilled in the art that the location of the toner as well as the exact color of the toner may vary. Each image forming unit 100A-100D comprises corresponding PC units 102A-102D and transfer devices 108A-108D. A voltage is applied to the transfer devices 108A, 108B of the first and second image forming units 100A, 100B using a shared high voltage power supply 120. Similarly, a voltage is applied to the transfer device 108C of the third image forming unit 100C using a second high voltage power

supply 122 and a voltage is applied to the transfer device 108D of the fourth image forming unit 100D using a third high voltage power supply 124.

[0021] The process of transferring an image onto the media substrate occurs sequentially starting at the first image forming unit 100A. The desired image is transferred to the media substrate line-by-line as the above process is repeated for each image forming unit 100 to produce the desired color and image. The media substrate is moved along the primary media path and to each image forming unit 100A-100D by the media transport belt 20. Accordingly, different layers of toner, starting with black and followed by yellow, magenta and cyan in the present embodiment, are added to the media substrate to produce the desired color and image. As is well known in the art, the exact color produced on the media substrate will depend on the toner transferred as well as the intensity thereof.

[0022] In most situations, the color black will be produced using black toner irrespective of the other colors. Accordingly, if the particular portion of the image is black, the other three toner colors will not be used. Conversely, if a particular portion of the image is a color other than black, black toner will not be used. Thus, for any given image, only one layer of toner will be applied to the media substrate by the first two image forming units 100A, 100B. This feature, in part, allows the first two transfer devices 108A, 108B to share the single high voltage power supply 120, significantly lowering system costs. In some applications, the image forming device 10 may be utilized by some users in black-only mode, in which only the image forming unit 100A is installed in the device 10. In other applications, the full color capabilities of the image forming device 10 will be exploited, with all image forming units 100A-100D installed. Thus, the high voltage power supply 120 may be called upon to drive either one or two image transfer devices (e.g., 108A alone or both 108A and 108B).

[0023] The transfer process is carried out by mechanically assisted electrostatic

transfer. The toned image developed on each PC unit 102A-102D is transferred to the media substrate by applying a more positive charge on the media substrate than that of the toner charge. The transfer devices 108A-108D provide the necessary transfer current to charge the media substrate based on the voltage potential established by the high voltage power supplies 120, 122, 124. The impedances of transfer devices 108A-108D vary in response to a number of factors, including temperature and relative humidity. According to the present invention, the impedance of the transfer devices 108A-108D are measured by varying the voltage applied by high voltage power supplies 120, 122, 124, and monitoring the resulting transfer current. The determined impedance is then used to set the voltage of the high voltage power supplies 120, 122, 124 to achieve the proper transfer current at the transfer devices 108A-108D.

[0024] The voltages output by the high voltage power supplies 120, 122, 124 are controlled by Pulse-Width Modulated (PWM) control signals 140, 142, 144, respectively, output by a controller (not shown). PWM control is well known in the art. By altering the duty cycle of a PWM control signal, the output of a high voltage power supply 120, 122, 124 may be varied from 0V_A (at 0% duty cycle) to the power supply's maximum output voltage (at 100% duty cycle). *or a more negative initial voltage positive*
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PUS 3/26/04 PUS 3/26/04
MXR 3/26/04 MXR 3/26/04

[0025] According to one embodiment of the present invention, the voltage at a given high voltage power supply 120, 122, 124 is ramped up, by altering the duty cycle of the corresponding PWM control signal 140, 142, 144, and the resulting current through the corresponding transfer device 108A-108D is monitored. Figure 4A depicts a graph of the transfer voltage as a function of the transfer current for a representative high voltage power supply 120, 122, 124. From this voltage/current relationship, the impedance of the corresponding transfer devices 108A-108D may be determined. This impedance may then be utilized to select the output voltage of the high voltage power supply 120, 122, 124 to yield the desired transfer current. Because the voltage/current relationship

is generally linear the impedance of a transfer device 108A-108D need only be calculated at one point along the graph of Figure 4A. This process is explained with reference to high voltage power supply 120, considering first the case of only one image transfer unit 100A installed in the image forming device 10, and then considering the case of all image transfer units 100A-100D being installed.

[0026] The output voltage of the high voltage power supply 120 is gradually ramped up by altering the duty cycle of PWM control signal 140. When the current through the transfer device 108A reaches a predetermined first transfer threshold current I_{XFER1} , such as 8uA, a transfer current threshold indicating signal 141 transitions from a low to high logic level, as depicted in Figure 4B. The generation of this signal is explained with reference to Figure 5, depicting a functional schematic diagram of a representative transfer current threshold indicating signal generating circuit. In the circuit of Figure 5, high voltage power supply 120 drives a resistive divider network comprising load resistance R_L representing, in this case, the impedance of the transfer device 108A, and a sense resistance R_{SENSE} of a predetermined value. The common node of R_L and R_{SENSE} is connected to the noninverting input of a first comparator C_1 . The inverting input of the first comparator C_1 is connected to a first reference voltage V_{REF1} , wherein:

[0027] $V_{REF1} = R_{SENSE} * I_{XFER1}$ or $V_{REF1} = R_{SENSE} * 8uA$

[0028] The output of the first comparator C_1 is connected to pull-up resistor R_1 , and output to a high voltage power supply controller (not shown).

[0029] The operation of the circuit of Figure 5 is straightforward, and explained with reference to Figures 4A and 4B in the case of one image transfer device 108A. Initially, the output of the high voltage power supply 120 is low or zero, and the reference voltage V_{REF1} exceeds the voltage drop across the resistor R_{SENSE} , resulting in a low level at the output of the first comparator C_1 . As the high voltage power supply 120 ramps up through its output voltage values (in response to a PWM control signal), the current

through transfer device 108A increases, as shown in Figure 4A. When the current reaches the first threshold current I_{XFER1} (for example, 8uA), the voltage drop across the resistor R_{SENSE} exceeds the reference voltage V_{REF1} , causing the output of the first comparator C_1 to switch from a low level to a high level.

[0030] The voltage power supply 120 controller (not shown) calculates the impedance of the transfer device 108A from the known voltage output level at the time of the first transfer threshold current I_{XFER1} (such as 8uA), as indicated by the transfer current threshold indicating signal 141. From this calculated impedance value, the controller may determine the proper output voltage for the high voltage power supply 120 to yield the desired transfer current for the transfer device 108A. This determination may be made via look-up tables, calculations according to known parametric equations or the like, as well known in the art. The circuit and method explicated above is applicable to high voltage power supplies 122 and 124, receiving PWM control signals 142, 144 to alter output voltage, and outputting transfer current threshold indicating signals 143, 145, to determine the proper output voltage to drive a desired transfer current through transfer devices 108C and 108D, respectively.

[0031] In full-color printing applications, the high voltage power supply 120 drives two image transfer devices 108A and 108B. In this case, the high voltage power supply 120 must provide transfer current to both devices 108A, 108B, and hence must provide nominally twice the transfer current (e.g., 16uA) as in the case of a single transfer device 108A. The method of determining the impedances of the transfer devices 108A and 108B, and adjusting the output voltage of the high voltage power supply 120 accordingly to generate the desired transfer current is applicable to the case of driving two transfer devices. According to the present invention, however, a single circuit determines both transfer current thresholds and outputs a single transfer current threshold indicating signal 141 indicative of both threshold currents.

[0032] The circuit of Figure 5 additionally includes a second comparator C_2 . The common node of R_L and R_{SENSE} is connected to the inverting input of the second comparator C_2 . The inverting input of the second comparator C_2 is connected to a second reference voltage V_{REF2} , wherein:

[0033] $V_{REF2} = R_{SENSE} * I_{XFER2}$ or $V_{REF1} = R_{SENSE} * 16\mu A$

[0034] The output of the first and second comparators C_1 , C_2 are connected together to form a combined transfer current threshold indicating signal 141, which is output to a controller (not shown) of the high voltage power supply 120. The output 141 is pulled high by pull-up resistor R_1 , and effectively forms a wired-AND function between the first and second comparators C_1 , C_2 . That is, both C_1 AND C_2 must output a high level for the transfer current threshold indicating signal 141 to be high. Conversely, if either C_1 OR C_2 output a low level, the transfer current threshold indicating signal 141 is pulled to a low level.

[0035] In operation, the output of the second comparator C_2 remains high so long as the second reference voltage V_{REF2} exceeds the voltage drop across the resistor R_{SENSE} . As the output voltage of the high voltage power supply 120 continues to increase following the low-to-high transition of the first comparator C_1 (indicating 8uA transfer current), the transfer current increases. When the current reaches the second threshold current I_{XFER2} (for example, 16uA), the voltage drop across the resistor R_{SENSE} exceeds the reference voltage V_{REF2} , causing the output of the second comparator C_2 to switch from a high level to a low level, pulling down the transfer current threshold indicating output signal 141, as depicted in Figures 4A and 4B.

[0036] Thus, the combined transfer current threshold indicating signal 141 outputs a logic low level if the transfer current is below I_{XFER1} (e.g., 8uA) or above I_{XFER2} (e.g., 16uA). The combined transfer current threshold indicating signal 141 outputs a logic high level if the transfer current is between I_{XFER1} and I_{XFER2} (e.g., 8-16uA). The voltages

at the threshold currents between I_{XFER1} and I_{XFER2} may be verified by ramping the output voltage of high voltage power supply 120 back down, and noting the low-to-high transition as the transfer current falls below I_{XFER2} (e.g., 16uA) and the high-to-low transition as the transfer current falls below I_{XFER1} (e.g., 8uA). Those of skill in the art will recognize that the same determinations of output voltages as the predetermined threshold transfer currents could be performed by initially setting the output voltage of the high voltage power supply 120 to a very large value by the PWM control signal 140, and gradually decreasing the output voltage while monitoring the threshold currents.

[0037] In one embodiment of the present invention, the voltage range of the first and second high voltage power supplies 120, 122 is substantially the same with a range from 0 V to 2.6 kV. In addition, the voltage range of the third high voltage power supply 124 is 0 V to 4.7 kV. The higher voltage range for the last image forming unit 100D enables reliable image transfer on highly resistive print media such as transparencies, vinyl labels and the like. Sharing the power supply for the first and second image forming units 100A, 100B and lowering the voltage range of all of the power supplies helps to reduce the overall hardware costs.

[0038] As additional layers of toner are added to the media substrate, it may be necessary to increase the voltage applied to the transfer devices 108C, 108D to compensate for the voltage potential of the already applied toner and the media substrate itself. The increased voltage allows for reliable image transfer.

[0039] In one embodiment of the present invention, the output voltage from the shared high voltage power supply 120 is fixed such that the voltage applied to the first two image forming units 100A, 100B is substantially the same. In another embodiment of the present invention, there may be a fixed offset between the voltages applied to the first two image units 100A, 100B. As shown in Figure 4, the shared high voltage power supply 120 comprises a voltage supply 130 and a voltage regulator 132. The voltage

6 JDR 3/26/04 PLS 3/26/04 MAX 3/26/04

regulator 132 comprises a resistive element 134 and a Zener diode 136. The voltage across the Zener diode 136 and output at port 138 is a fixed offset from the voltage generated by voltage supply 130 and output at port 140. The first transfer device 108A is coupled to port 138 while the second transfer device 108B is coupled to port 140. While the transfer devices 108A, 108B for the first and second image forming units 100A, 100B received different voltages, the voltages are supplied by the shared high voltage power supply 120.

[0040] As the image transfer process progresses, the voltages applied to the transfer devices 108A-108D may vary depending whether the media substrate is between the transfer nip or between image forming units (interpage gap). In one embodiment of the present invention, the voltage applied to the transfer devices 108A, 108B of the first and second image forming units 100A, 100B will be substantially the same with the media substrate between the transfer nips of the first two units or during the interpage gap. However, the voltage applied to the transfer devices 108C, 108D of the third and fourth image forming units 100C, 100D during the interpage gap will be lower than when the media substrate is between the transfer nip of the last two units. It will be apparent to those skilled in the art that the voltage during the interpage gap and when the media substrate is between the transfer nip may remain substantially the same for each image forming unit or vary as necessary.

[0041] The transport belt 20 is illustrated in the embodiments for moving the media sheets past the image forming units 100, and as part of the subunit 13. In another embodiment, roller pairs are mounted to the subunit 13 and spaced along the media path. The roller pairs move the media sheets past the image forming units 100. In one embodiment, each of the roller pairs are mounted on the subunit 13. In another embodiment, one of the rollers is mounted on the subunit 13, and the corresponding roller of the pair is mounted on the main body 12.

[0042] Although the present invention has been described herein with respect to particular features, aspects and embodiments thereof, it will be apparent that numerous variations, modifications, and other embodiments are possible within the broad scope of the present invention, and accordingly, all variations, modifications and embodiments are to be regarded as being within the scope of the invention. The present embodiments are therefore to be construed in all aspects as illustrative and not restrictive and all changes coming within the meaning and equivalency range of the appended claims are intended to be embraced therein.